



JAIDEV EDUCATION SOCIETY'S
J D COLLEGE OF ENGINEERING AND MANAGEMENT
KATOL ROAD, NAGPUR
Website: www.jdcoem.ac.in E-mail: info@jdcoem.ac.in
(An Autonomous Institute, with NAAC "A" Grade)
Affiliated to DBATU, RTMNU & MSBTE Mumbai



VISION

To win the trust of all stakeholders in conducting the assessment and evaluation.

MISSION

1. To frame and adopt procedure for various works involved in accountability.
2. To utilize the resources as per expertise of individual and maintaining good quality and standard of assessment work.
3. To ensure that the student participate in assessment process enthusiastically.

OFFICE OF CONTROLLER OF EXAMINATIONS

**Time Table of M.Tech I-Sem Summer 2024 (Supplementary) Examination
Theory Examination (Session- 2023 - 2024 Batch)**

Time: 10.30 AM to 01.30 PM

Date: 25-06-2024

Branch / Day & Date of Exam	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
	01/07/2024	02/07/2024	03/07/2024	04/07/2024	05/07/2024	06/07/2024
Computer Science & Engineering	Computer Algorithms	Advanced Operating System	Elective-I	High Performance Computing	Open Elective-I	Graphics Designing
Electronics & Telecommunication Engineering	High Speed Semiconductor Devices & Circuits	VLSI System Design	Professional Elective-I	Computational Methods	Open Elective -I	---

Practical Examination (Session- 2023 - 2024 Batch)

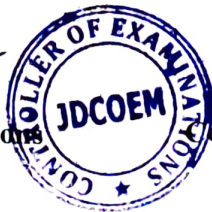
Time: 01.30 PM to 04.30 PM

Branch / Day & Date of Exam	Monday	Tuesday	Wednesday
	01/07/2024	02/07/2024	03/07/2024
Computer Science & Engineering	Computer Algorithms	Advanced Operating Systems	---
Electronics & Telecommunication Engineering	PG Lab	VLSI System Design	Graphic Design

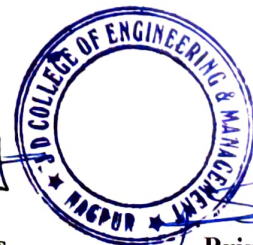
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Controller of Examinations



Principal
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J D College of Engineering & Management
Khandala, Katol Road
Nagpur-441501



Education to Eternity

Jaidev Education Society's
J D College of Engineering & Management, Nagpur
 (An Autonomous Institute, Affiliated to Dr. Babasaheb Ambedkar Technological University of Maharashtra)
Examination Cell



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OFFICE OF CONTROLLER OF EXAMINATIONS
Time Table of M.Tech I-Sem Summer 2024 (Supplementary) Examination
Theory Examination (Session - 2019 - 2023 Batch)
Time: 10.30 AM to 01.30 PM **Date: 25-06-2024**

Branch / Day & Date of Exam	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
	01/07/2024	02/07/2024	03/07/2024	04/07/2024	05/07/2024	06/07/2024
Computer Science & Engineering	Computer Algorithms	Advanced Operating System	Advanced Computer Network	Elective I	Elective II	Communication Skill
Electronics & Telecommunication Engineering	Computational Methods	Embedded System Design	VLSI System Design	Elective I	Elective II	Research Methodology

Practical Examination (Session - 2019 - 2023 Batch)
Time: 01.30 PM to 04.30 PM

Branch / Day & Date of Exam	Monday	Tuesday
	01/07/2024	02/07/2024
Computer Science & Engineering	Advanced Operating System Lab	—
Electronics & Telecommunication Engineering	Embedded System Design Lab	VLSI System Design Lab

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